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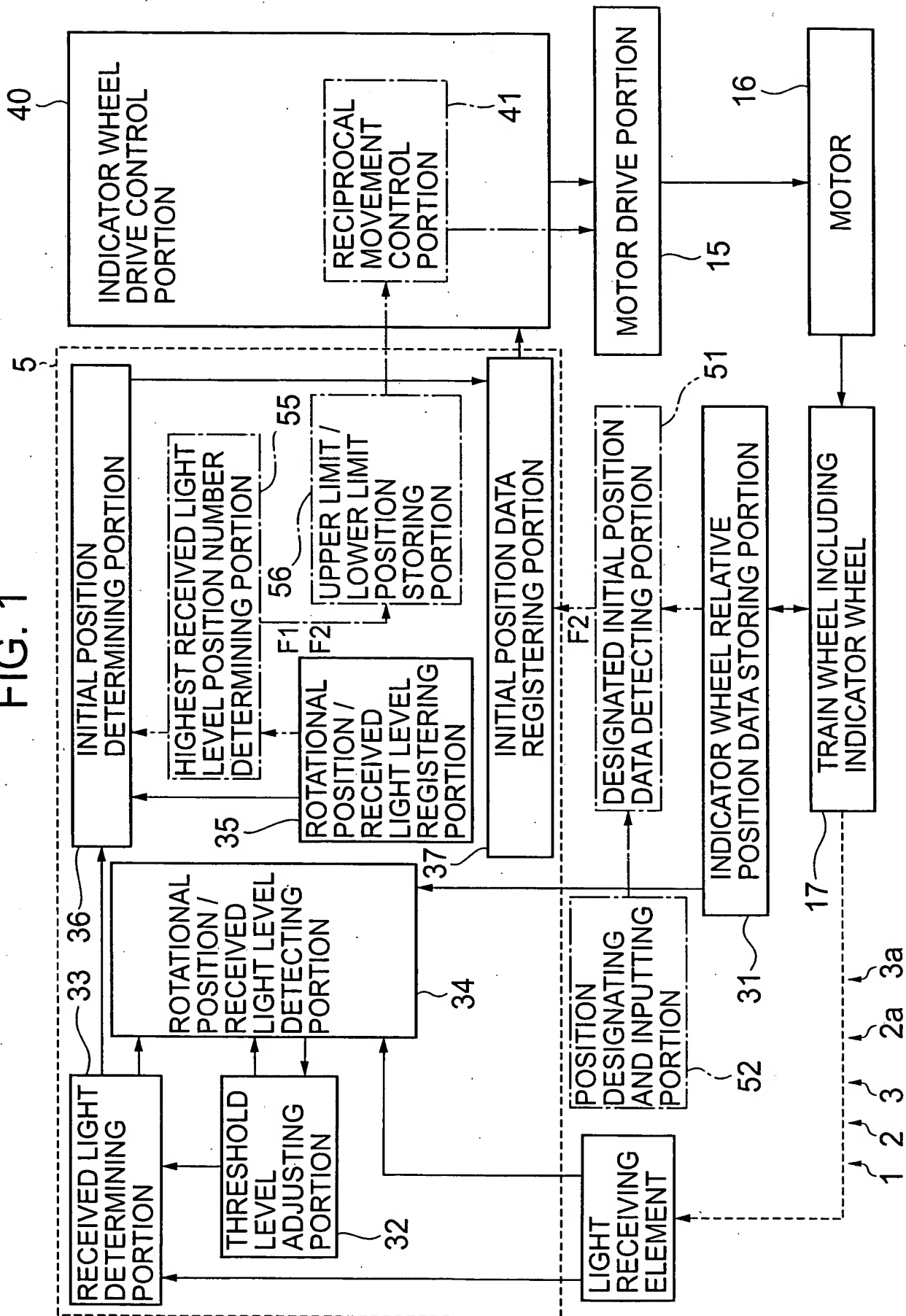
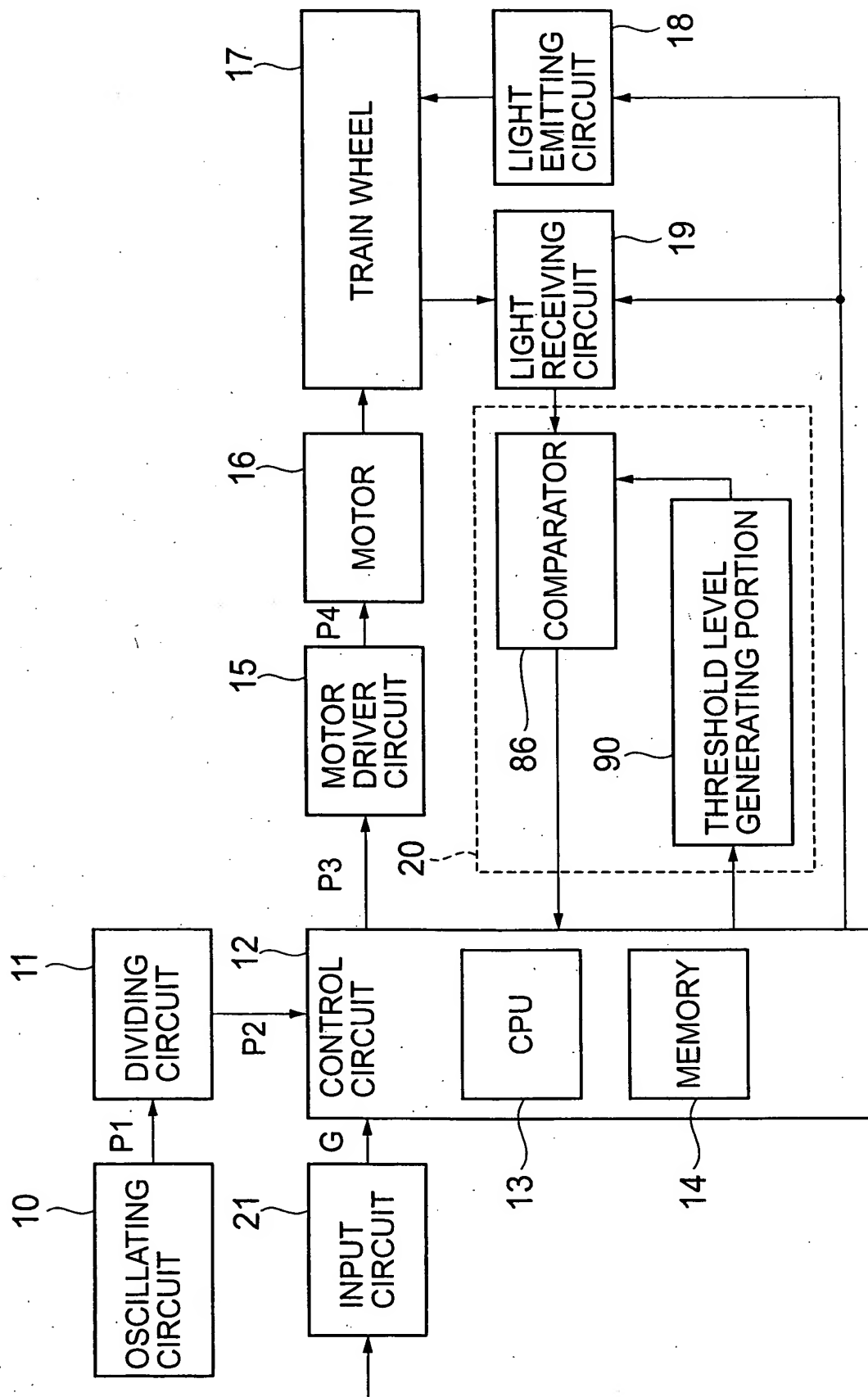
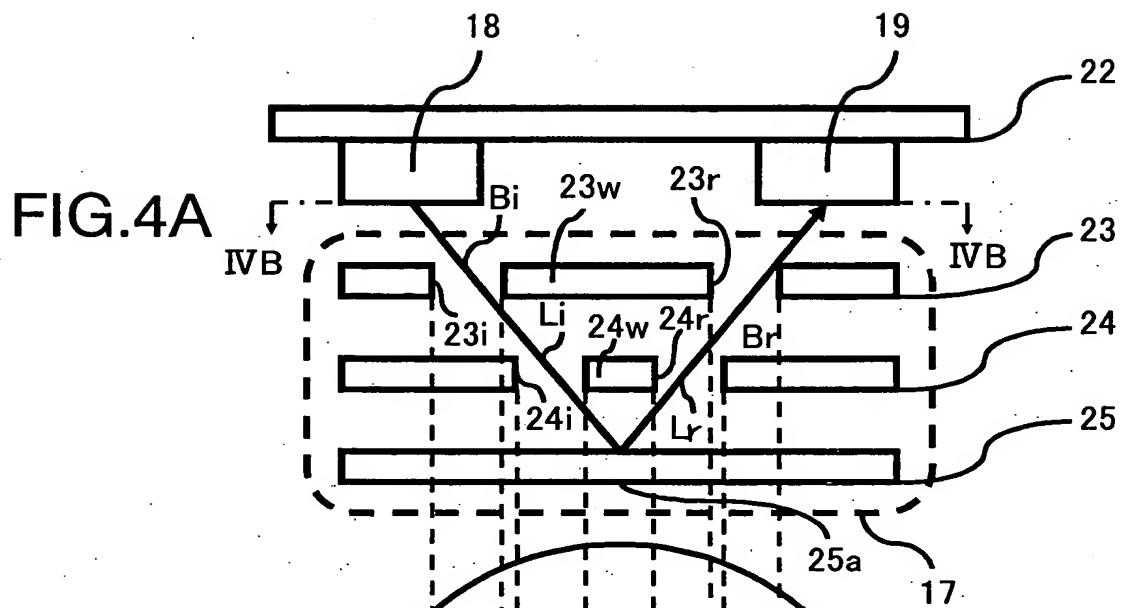


FIG. 2







**FIG.4B**

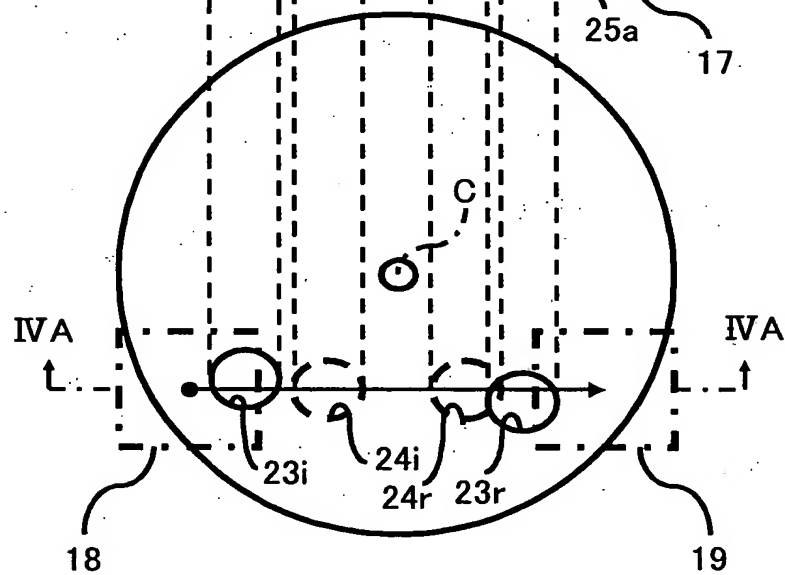


FIG. 5

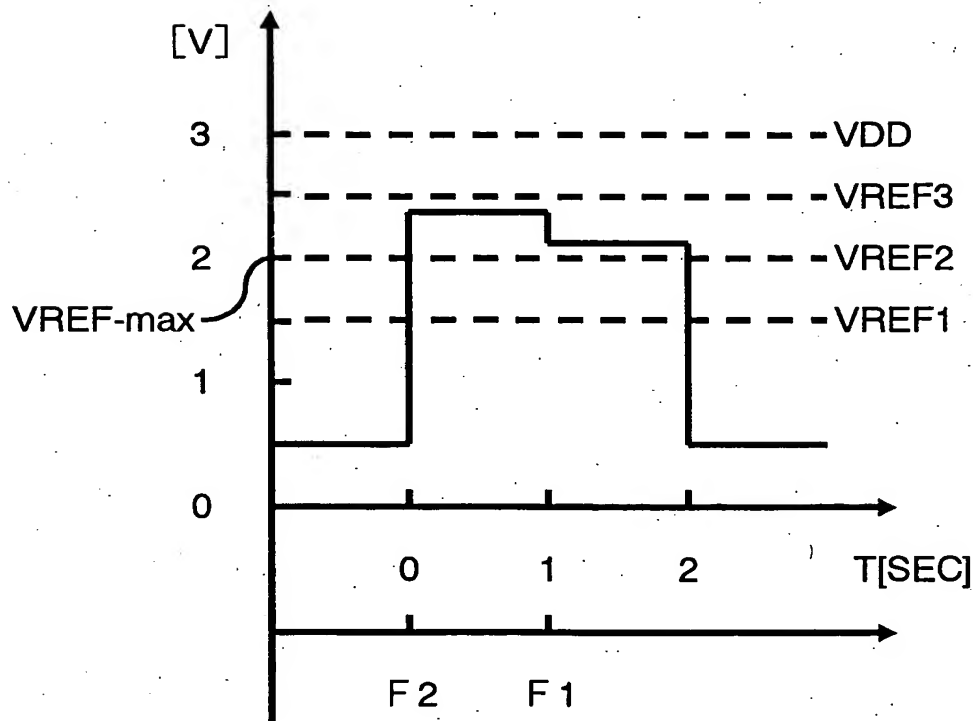
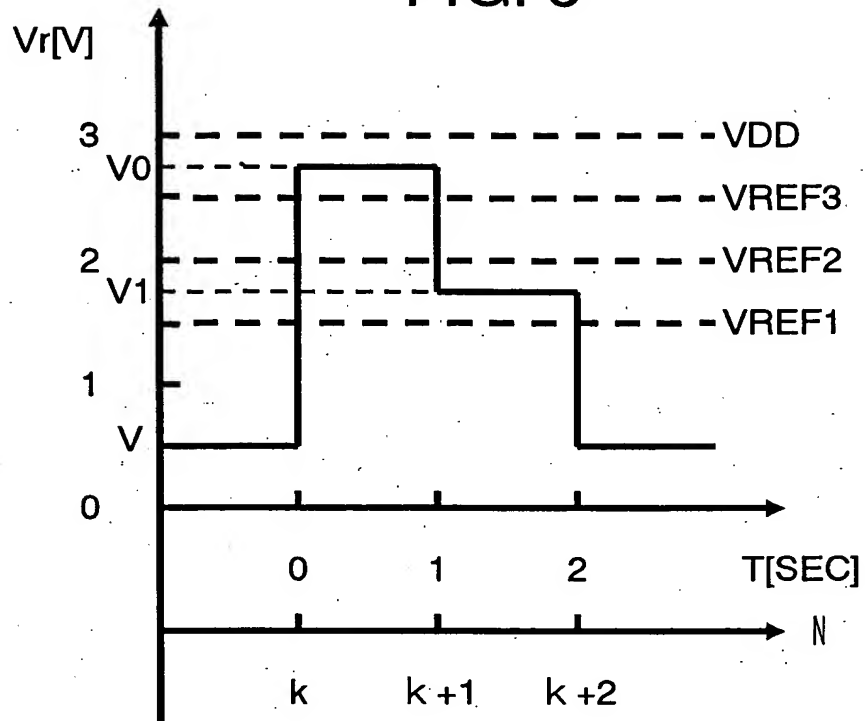


FIG. 6

FIG. 7

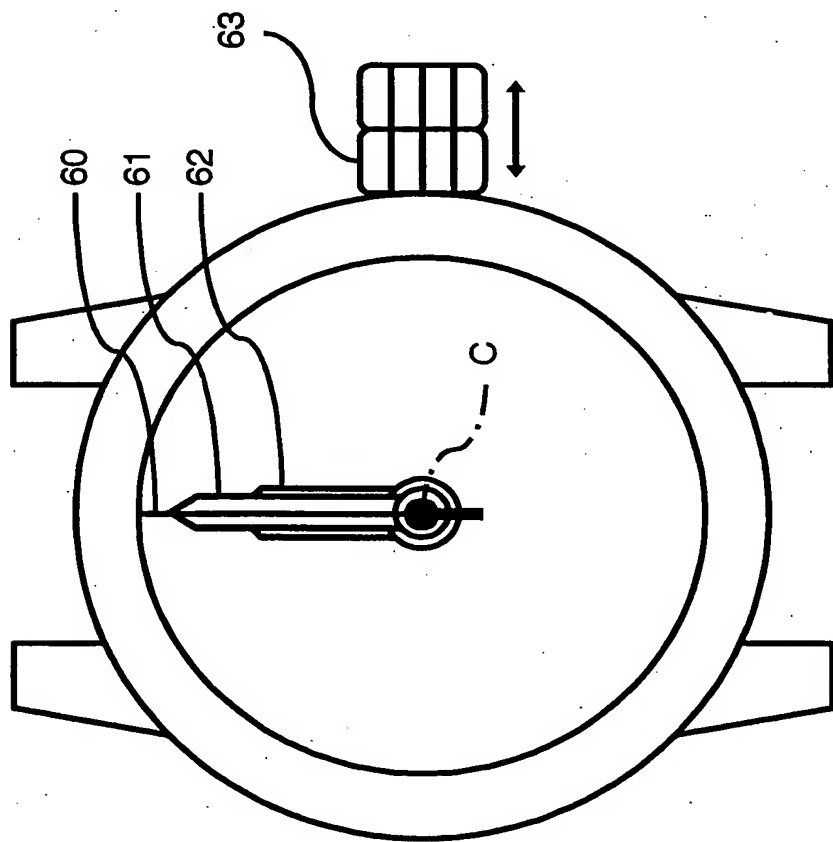


FIG. 8

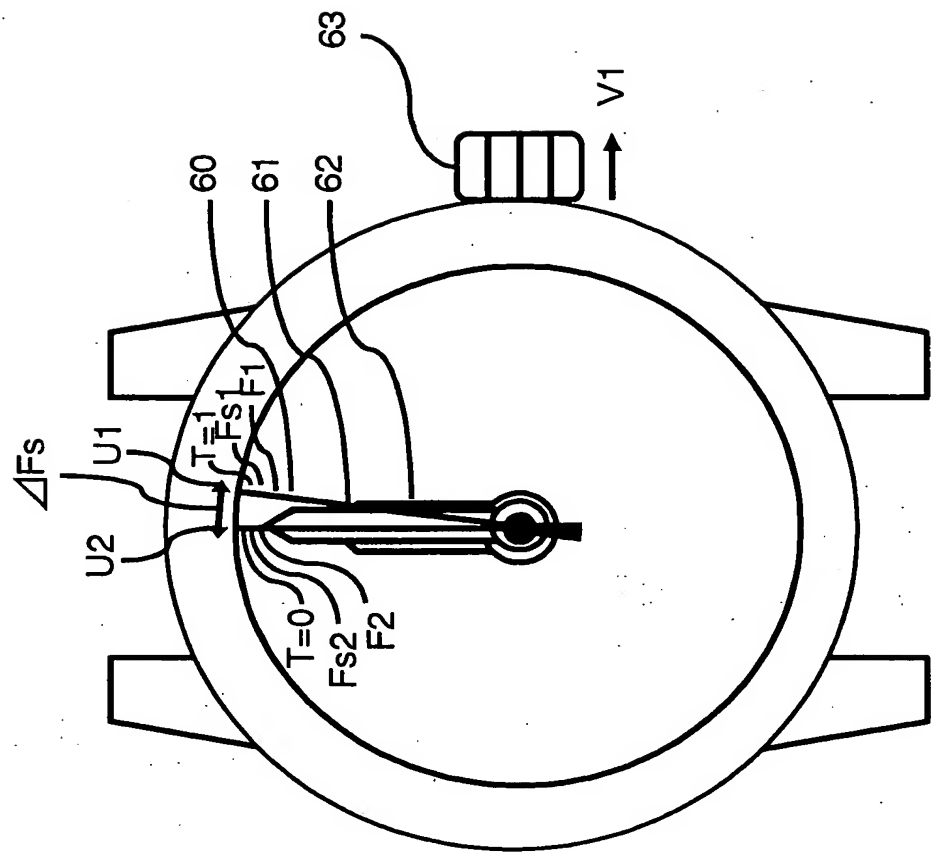


FIG. 9

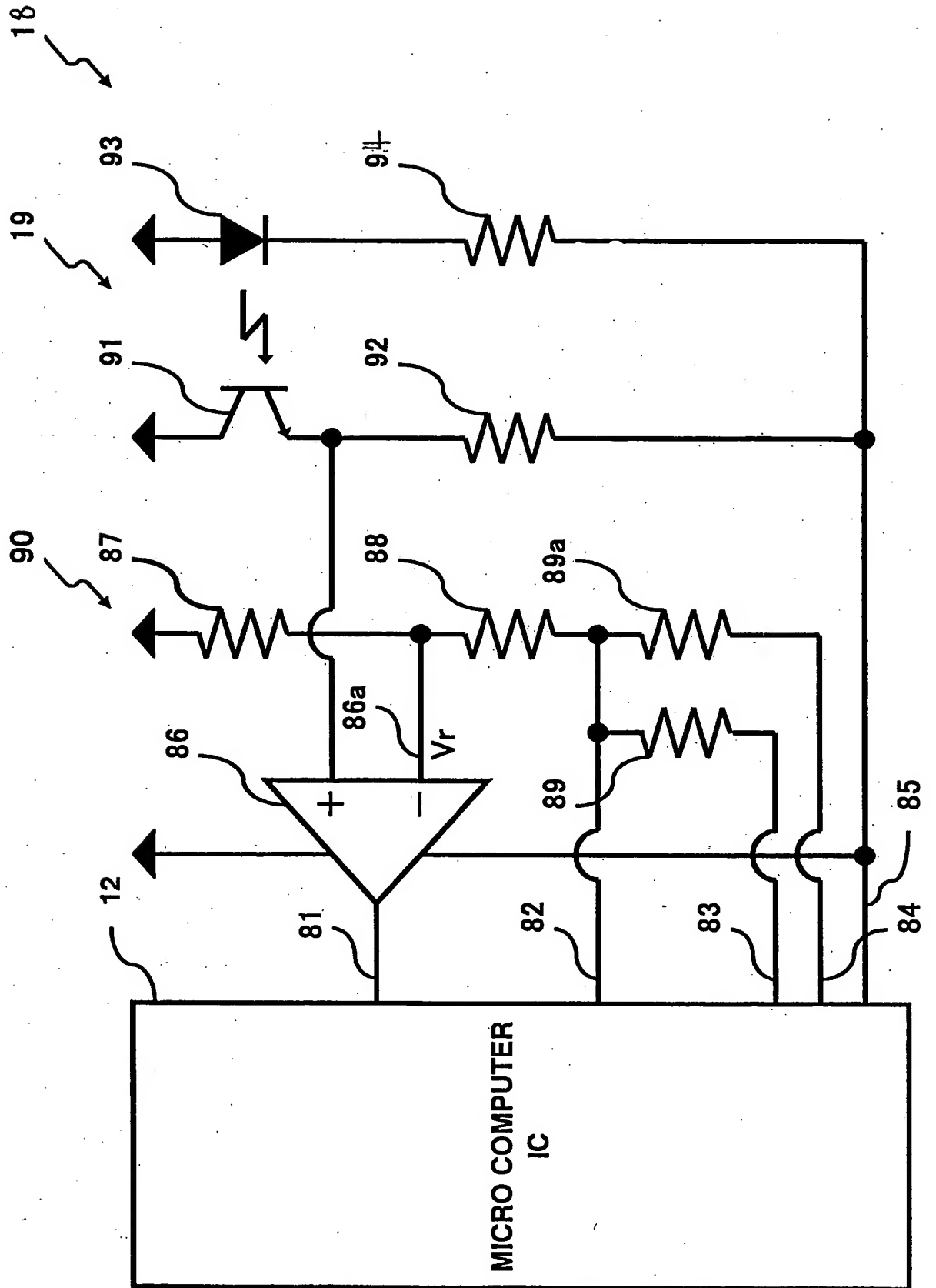


FIG. 10

REFERENCE VOLTAGE		PORT 82	PORT 83	PORT 84
VREF1	1.5V	VSS	Hi-Z	Hi-Z
VREF2	2.0V	Hi-Z	VSS	Hi-Z
VREF3	2.5V	Hi-Z	Hi-Z	VSS

FIG. 11

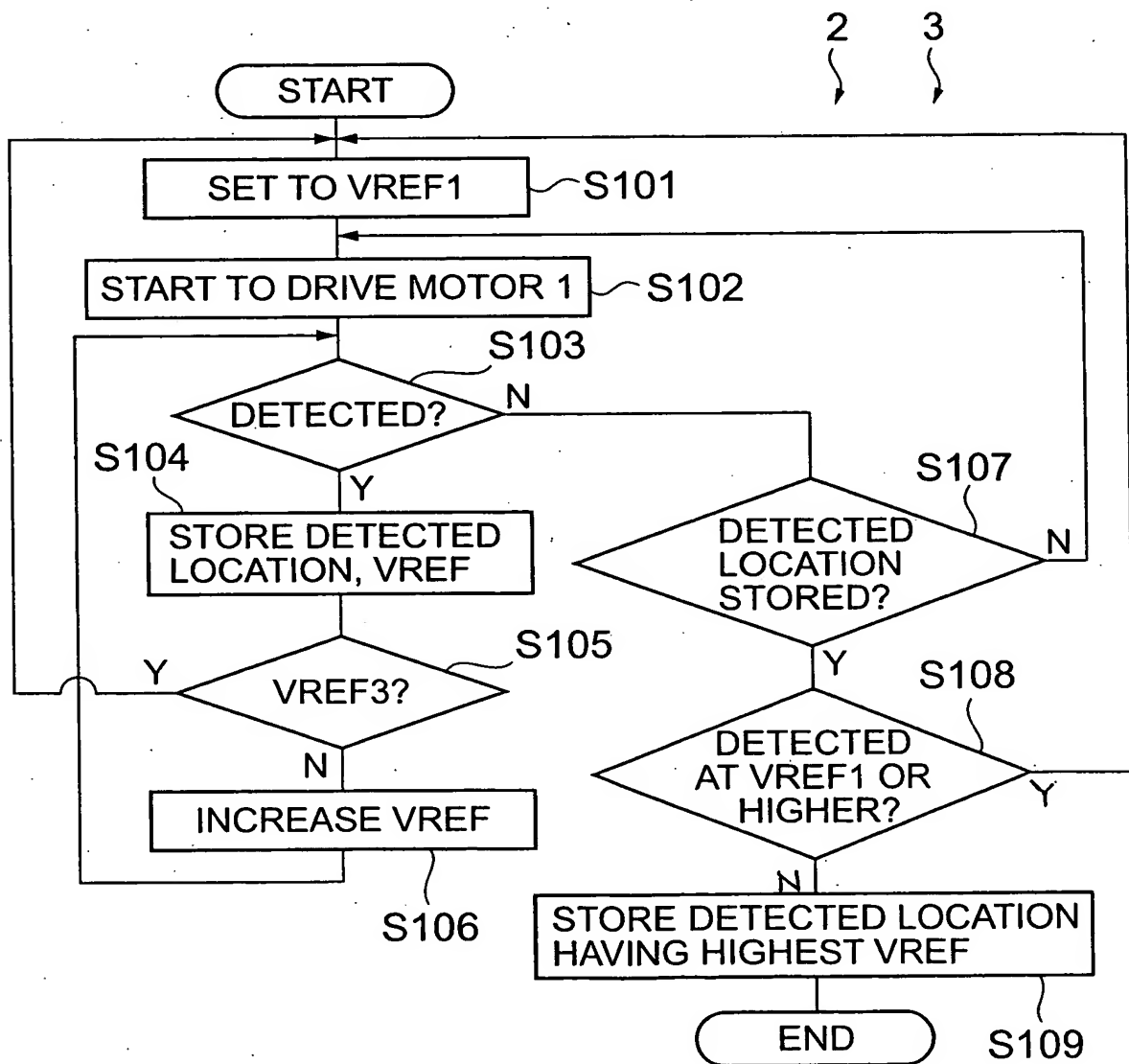




FIG. 12

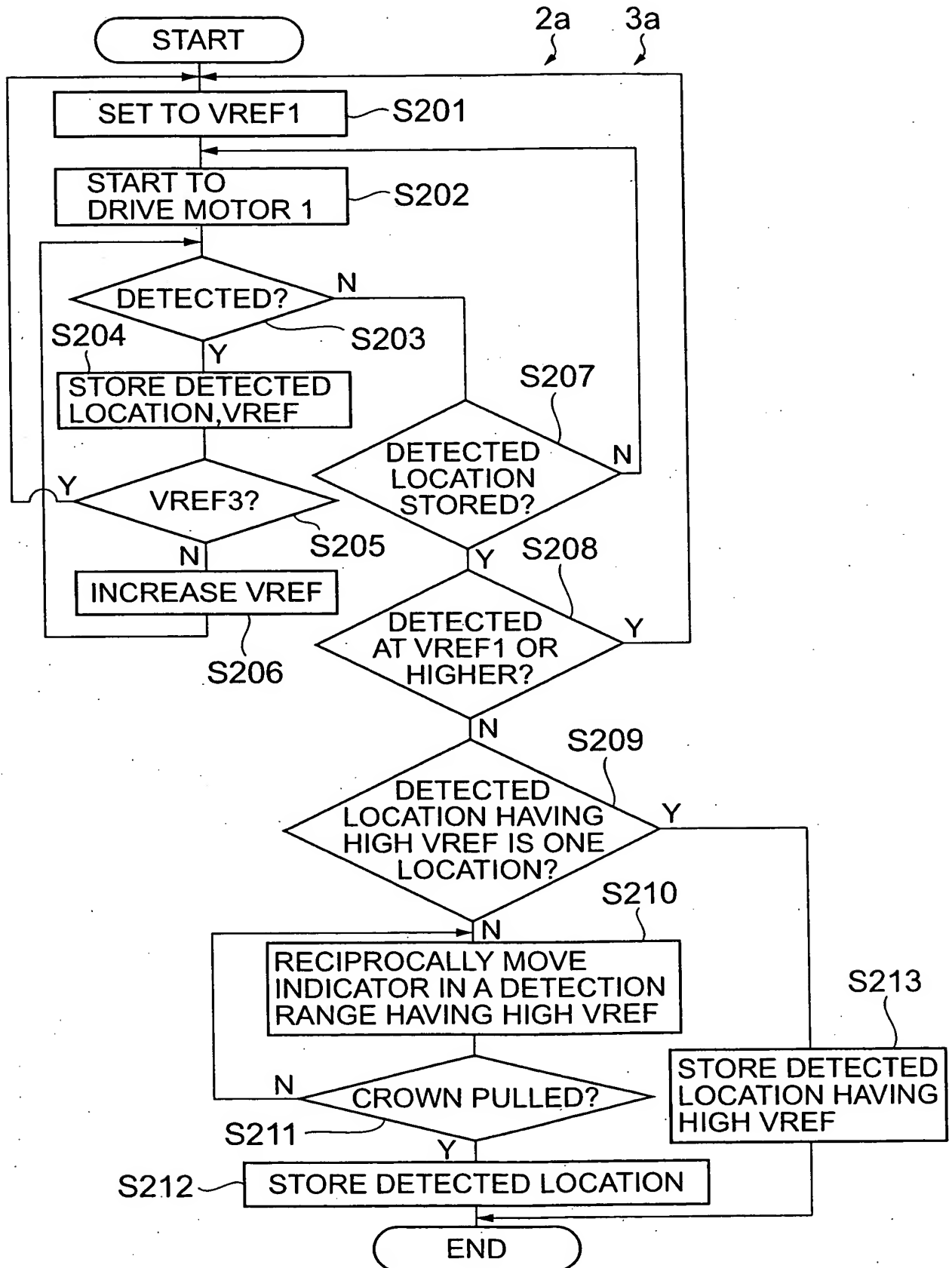


FIG. 13

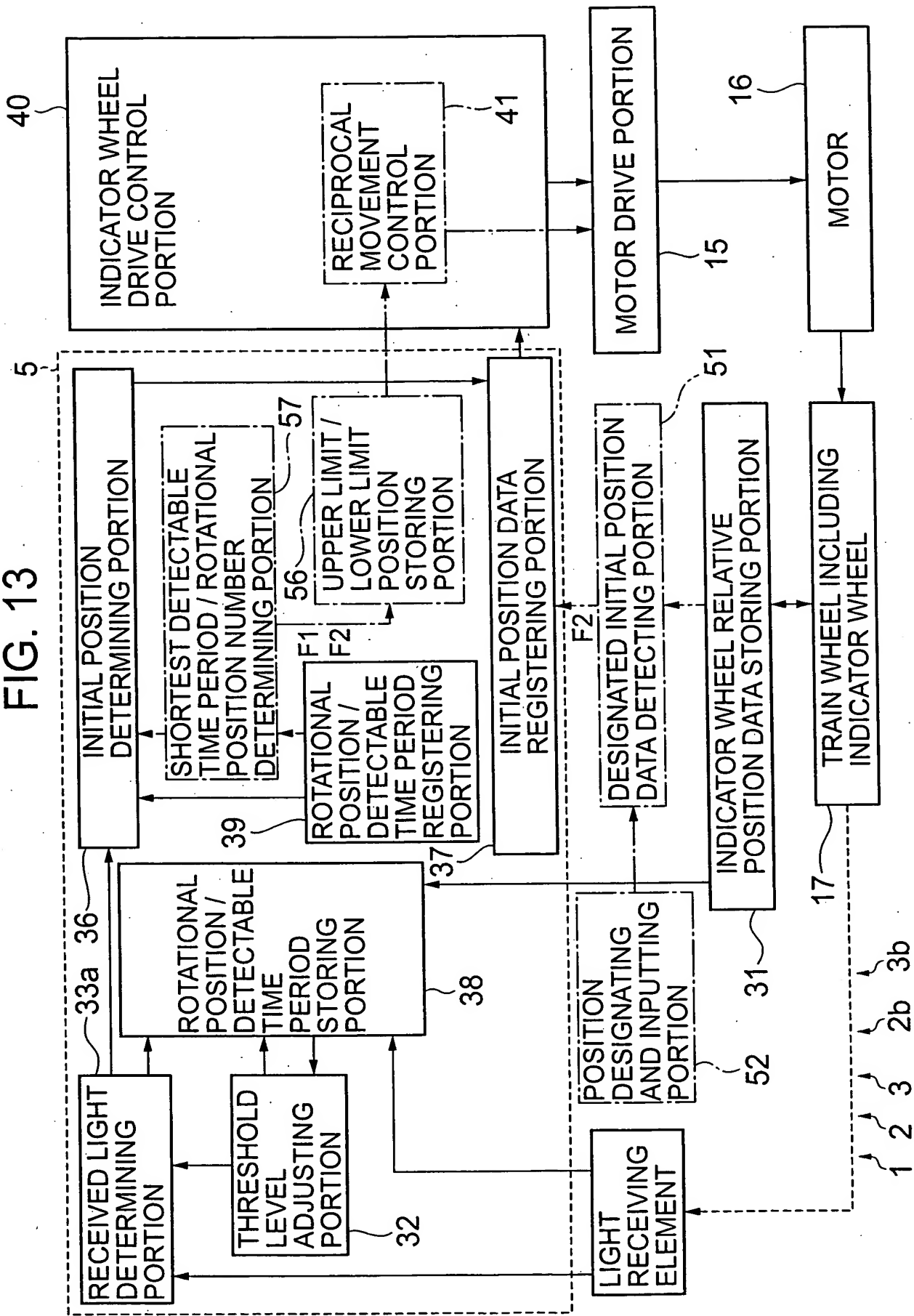


Fig. 14

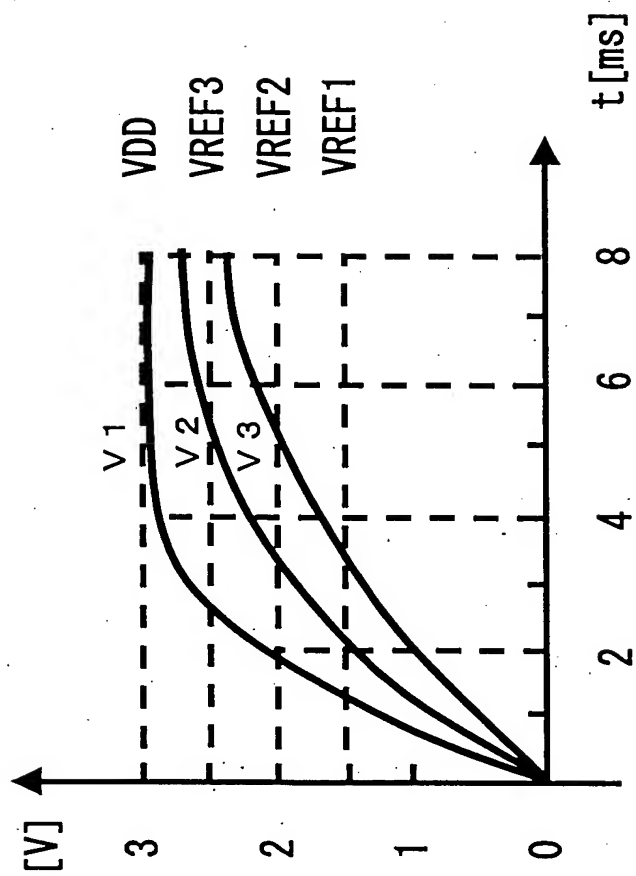
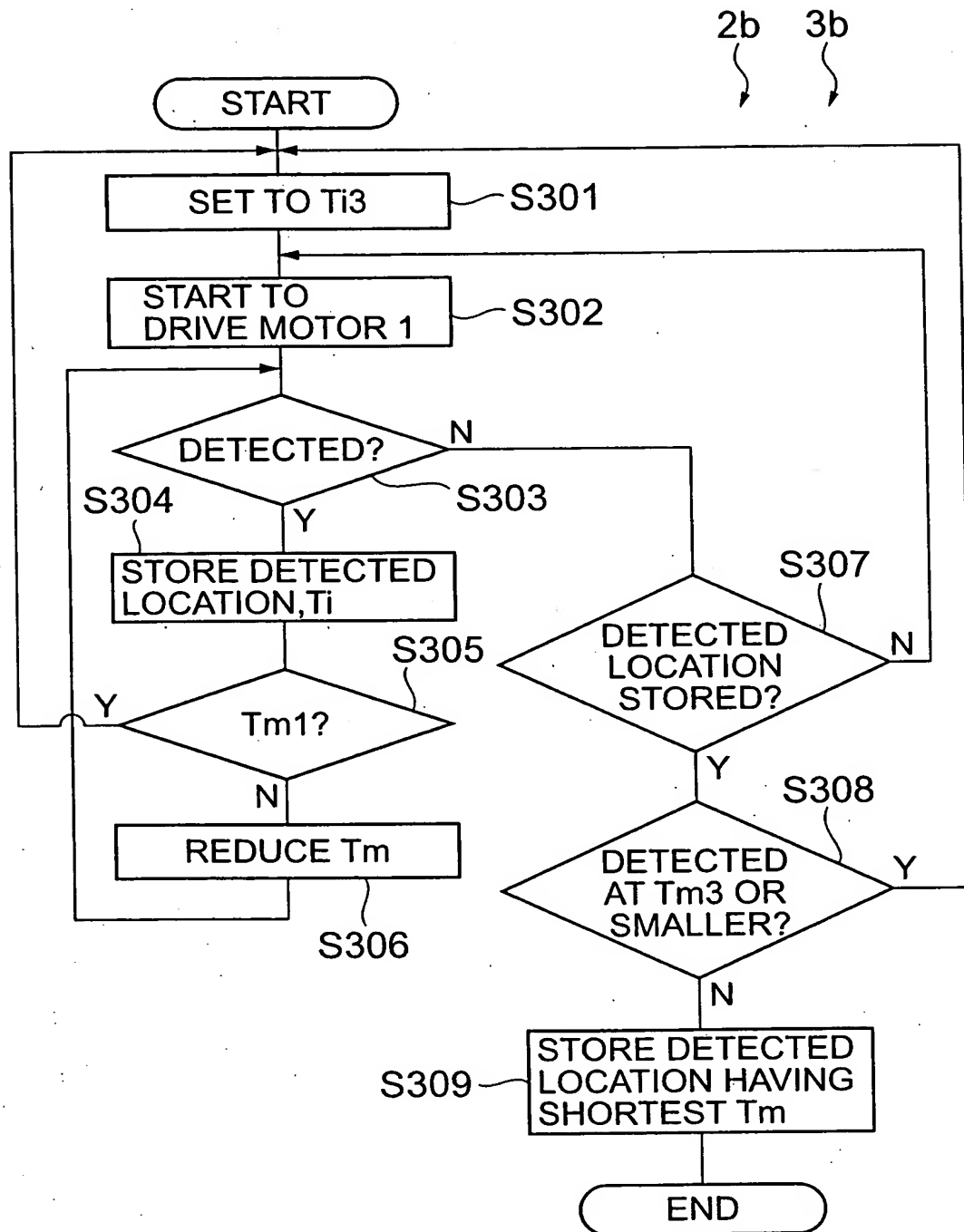


FIG. 15



$Tm1 < Tm2 < Tm3$   
IN DETECTING BY LIGHT RECEIVING ELEMENT

FIG. 16

